

# CV

1.Name: Dr. T.Satya Savithri

2.Designation& Address: Professor  
Dep. of Electronics and Communication Engineering  
JNTUH College of Engineering, Kukatpalli  
Hyderabad, Telagana State, India-500085.

Email : [tirumalasya@gmail.com](mailto:tirumalasya@gmail.com) , [tirumalasya@jntuh.ac.in](mailto:tirumalasya@jntuh.ac.in)

Mobile No. 9866706183.

### 3.Academic Qualifications:

| Examinations                                   | Name of the Board / University                           | Year of passing | Percentage of marks obtained | Division/ Class/ Grade | Subject                              |
|--|--|-----------------|------------------------------|------------------------|--------------------------------------|
| High School/ SSC                               | Govt. High School, Board of Secodary Edu, AP             | 1984            | 70%                          | First                  | Composite Maths                      |
| Diploma in Electronics and communication Engg. | KNPW, Hyderabad,State Board of Tech.Edu. A.P             | 1987            | 79.15%                       | First with Distinction | Electronics &Communication Engg      |
| Under Graduate<br>B.Tech.                      | REC Warangal Kakatiya Uni.                               | 1992            | 73.81%                       | First with Distinction | Electronics &Comm. Engg              |
| Post Graduate<br>M.E                           | College of Engg. Osmania                                 | 1997            | 70.72%                       | First                  | Microwave &Radar Engg.               |
| Ph.D*  | In the Dept. of Electronics and Communication Engg. JNTU | 2006            | ---                          | ---                    | Digital Image Restoration Techniques |

#### 4. Working Experience:

| Designation          | Department/college/uni       | Date of actual joining |            |
|----------------------|------------------------------|------------------------|------------|
|                      |                              | From                   | to         |
| R&D Engg.            | M/SFRONTIER Telecom Pvt Ltd  | 15-06-1992             | 04-08-1995 |
| Teaching Faculty     | CBIT Hyderabad               | 01-09-1995             | 30-09-1997 |
| Lecturer             | KSRM College of Engg. Kadapa | 03-10-1997             | 27-11-1999 |
| Asst. Professor      | ECE, JNTU                    | 29-11-1999             | 04-10-2002 |
| Asst. Professor. Sr. | ECE, JNTU                    | 04-10-2002             | 07-11-2006 |
| Assoc. Professor     | ECE, JNTU                    | 07-11-2006             | 31-12-2012 |
| Professor            | ECE, JNTUH                   | 01-01-2013             | Till date  |

#### Additional posts held:

| Designation                                       | Department/college/uni        | Date of actual joining |            |
|---|-------------------------------|------------------------|------------|
|   |                               | From                   | To         |
| Assoc. Professor & Coordinator                    | R&D Cell, JNTUH               | 27-06-2009             | 02-08-2010 |
| Assoc. Professor & Deputy Warden for Girls Hostel | JNTUH college of Engg., JNTUH | 03-08-2010             | 04-02-2011 |
| Professor & office-in-charge of examinations      | JNTUH college of Engg, JNTUH  | 23-09-2013             | 05-12-2015 |
| Professor & HOD                                   | ECE, JNTUH College of Engg,   | 23-11-2016             | 28-12-2018 |
| Professor & MOU coordinator                       | ECE, JNTUH College of Engg.,  | 07-05-2019             | Till date  |

#### 5. Interested Areas:

Signal and Image Processing, VLSI architectures, propagation models and channel estimation, Antenna designing, Satellite communication

#### 6. Publications:

International Journals: 54

National Journals: 5

International conference: 44

National conference: 9

Total : 112

H-index : 13

## **7. Patents applied:**

1. VLSI Architecture for Robot Navigation in Semi Structured Indoor Environment.
2. Depth Estimation of Non-rigid Shapes Based on Fibonacci Population Degeneration Particle Swarm Optimization

## **8. Funded Projects:**

1. Design and Development of CAD system to Detect Nodule in Chest Radiographs – Rs. 10.5 lacs from AICTE, 2017-2020
2. MODROPS-VLSI & ECAD Laboratory – Rs.10.42 lacs from AICTE-2017-2019
3. Reconfigurable SoC based Design for High Speed Data Acquisition System - TEQIP - 2,95,000 - 2019-2021

## **9. Ph. D Guided: 13**

## **10. Workshops/Refresher Courses/Short term courses:**

- (i) Attended : 27
- (ii) Conducted: 20

## **11. Expert Lecturers in the subjects of Image Processing, VLSI Design, Antennas etc., in various Engg Colleges and Public sectors like DLRL, NRSC etc.,: 45**

## **12. Session Chair in Technical Fest/Conferences/symposiums:15**

## **13. Reviewer:**

Journal of Electrical and Electronics Engineering Research( JEER), International Journal of Advances in Electrical and Electronic Engineering, VSB-Technical University of Ostrava, Elsevier -Computers in Biology and Medicine, IEEE Access, Technology Spectrum, JNTUH, e-learning material for the first year B.Tech(CCC) programme , JNTUH, College Magazine, JNTUH College of Engg,

## **14.Member/Convener in various committees:**

Subject experts in recruitment committees of DRDO, Departmental Promotion Interview Boards in ADRIN, NRSC, recruitment board member for lecturers in polytechniques, Member of: Studies Board of, Board of Governing body of affiliated colleges, Doctoral committee, Research Review committee, Fact Finding committee, Ragging committee, Procurement committee etc.,

## **15. Membership in Professional Bodies:**

Member of International Association of Engineers, Member of Institute of Electrical and Electronics Engg., Fellow of Indian Institute of Engineering , Fellow of Institute of Electronics and Telecommunication Engineering, Member of ISTE.